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Abstract-To solve the problem of traditional stream ciphers can not transmit through the public channel and the construction of high-strength cipher key generator, the paper presents the minimum power clock function driven by a parallel clock-control sequence and chaotic cascade function made of multiple Logistic function, which are used to built the self-reference model and chaotic module; it designs an adapt controller independent of the key generator and encrypt module, achieve controlling encryption and transmission process by multi-threaded code; it makes a high-strength key generator with sure differential transformation and pseudo-random horizontal level disturbance wheel key method of AES and S-box of Camellia, which makes the sub-key and the key generator's internal station become vogue and uncertain within controlled area that does not depend on the secret channel, the model achieves an exponential growth in the key space and the same application scope of traditional stream cipher model. The new protocol model features can be seen under the actual operation of modifying RC4 and real-time RC5 algorithm.

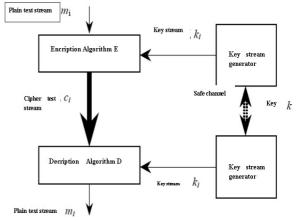
*Abstract*—Stream cipher; Minimum power clock function; Gaussian chaos cascade function group; Block ciphers; Sbox; Pseudo-random horizontal level disturbance

#### I. INTRODUCTION

Ciphers in cipher stream can be divided into Synchronous Stream Cipher, SSC and Self-Synchronous Stream Cipher, SSSC. SSC is widely used at present. The core of stream cipher is the key generator. In synchronous stream cipher, the generation of key stream is independent of the plaintext and ciphertext, see Figure 1, encryption transformation E and decryption transformation D are the time-varying function, the time variability is guaranteed by the memory file of encryption and decryption device. Figure 2 is the key stream generator model, in which g is a function of generating the key stream, f is a state function, g using the key seed k as input and produce an output key stream k1k2 ... . As long as the transmitter and receiver have the same key and the internal state, the same key stream can be generated, then the key generator of both the transmitter

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and receiver is synchronized. Once out of sync, decrypt failed immediately [1] ~ [5]. SSSC ommited.





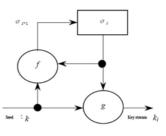


Figure 2. Key Stream Generator

The current construction method of key generator may be roughly divided into four categories: information theory approach, system theory approach, complexity theory approach and stochastic method [6] ~ [8]. In this paper, the author takes system theory and the principle of diffusion chaos of information theory as theoretical foundation, with adaptive control technology and chaotic mathematics to build SSC and SSSC protocol model and the corresponding key generator which can be easily realized and does not depend on secret channel. Since they share the same principle, we only take SSC as an example to give a specific introduction.  $[9] \sim [13]$ 

## II. CONSTRUCTION OF MINIMUM POWER CLOCK FUNCTION AND GAUSSIAN CHAOS CASCADE FUNCTION GROUP

$$T = \{T_{0},...,T_{k},...,T_{n}\} = \{t_{0},...L \ (t_{k,t_{k}}) + W^{t} t \text{ so } t_{k} \cdot e^{-t_{k} \cdot w_{k}} \cdot COS(W_{k} \cdot t_{s}),...T_{n}\}$$

$$W_{k} = w_{0} \oplus X_{k-1} \oplus Y_{k-1} \oplus Z_{k-1} \oplus T_{k-1}$$

$$T_{0} = T(t, w_{0}) = \begin{cases} t_{lass}, w_{0} = 1 \\ t_{now}, w_{0} = 0 \end{cases}$$

$$w_{0} = \begin{cases} 0, (w_{lc} - E(w_{1}) \neq 0, w_{lc} - w'_{lc} \neq 0) \\ 1, (w_{lc} - E(w_{1}) = 0, w_{lc} - w'_{lc} = 0) \end{cases}$$

When  $w_0=0$ , t is updated to the current time  $\hbar w_w$ , when  $w_0=1$ , t maintains the latest value  $\hbar w_w$  after last update.<sup>w</sup> is the *Logistic* sequence of the first stage N,  $w_{1c}$  is the encryption result of <sup>w</sup> through encryption algorithm E,  $w_{1c}$  is the return parameter of decryption end. The value of  $\hbar w_{1}$  and  $\hbar w_{2}$  are determined by clock sequence output function Ts. It is easier for clock output sequence to control the linear complexity than alignment sequence and non-linear combined sequences, by the construction of stimulus module through clock output signal controlling clock function, better pseudo-random and controlling can be achieved.

Chaotic sequence taking *Logistic* to generate cascade function:

$$E(f,K) = \beta \int_{\Omega} (f-g)^{2} dx + \int_{\Omega-K} |\nabla f|^{2} dx + \alpha |K|$$

$$f = Xn + 1 \oplus Yn + 1 \oplus Zn + 1, g = Xn - 1 \otimes Yn - 1 \otimes Zn - 1$$

$$\beta = t_{0} \otimes t_{k-1}, \alpha = t_{k-1} \oplus x_{k-2}, \Omega = \{x_{1}, \dots, x_{n-1}\}, \Omega - K = \{x_{n-k}, \dots, x_{n-1}\}$$

$$|K| = \sum_{k=1}^{k} X_{k} \cdot \sum_{k=1}^{k} Y_{k} \cdot \sum_{k=1}^{k} Z_{k}$$

$$\{X_{n+1} = T_{0} \cdot X_{n}(1 - X_{n})$$

$$Y_{n+1} = X_{n} \cdot Y_{n}(1 - Y_{n})$$

$$Z_{n+1} = Y_{n} \cdot Z_{n}(1 - Z_{n})$$
The effect level low encode is consist by  $[Y_{n}(\Omega)]$  [T(4, production)]

The first-level key space is consist by  $[X(0), [T(t_0,w_0)]$ , the second-level key space by [Y(0), X(0)], the thirdlevel by [Z(0), Y(0)], in which,  $X(0), Y(0), Z(0), T(t_0, w_0)$ are the initial value of each map respectively. The genusrandomness, ergodicity and broadband nature makes it difficult for the analysis to find time-domain and frequency-domain characteristics of encrypted signals. Chaotic signal class, making analysis difficult are. By constructing a chaotic cascade function, the single chaotic function key space can be exponentially enhanced. [14]~[18]

## III. CONSTRUCTION OF ADAPTIVE SSC MODEL AND KEY GENERATOR

# A. Adaptive Control Flow Chart and Reference Model Design

Adaptive control technology can realize signal selfdetection and self-adjusting. [19] ~ [21] The adaptive controller initialization within the control of continuous or intermittent output with automatic detection and adjustment function of the control signal, through the design of reference models or self-tuning controller module can be achieved on the output or received signal real-time adjustment and dynamic match. Adaptive encryption control principle is as Figure 3:

First initialize the clock module and the clock stimulus module as a self-reference model, since the self-reference model will reconstruct when the detective signal received from the self-detection module does not match, and the re-constructed reference model is not dependent on external stimulation, which depends only on the initial algorithm  $\mathcal{F}$ . This means that as long as both encryption and decryption have the same reference model, after the same initialization, they can always get synchronous control signal. For example, the use of the two CMOS unit can keep output synchronized at  $k \cdot 10^8 / S$ 

Take the output signal from the self-reference model as the first stage parameter of chaos cascade module, the output signal of the first stage of chaotic module as the input signal of self-tuning module and at the same time, as the input signal of the second stage *Logistic* generator; the output signal of the second stage *Logistic* generator as the input signal of the third stage or input signal of key generator; the output signal of the third stage and key k together as the initial key of key generator.

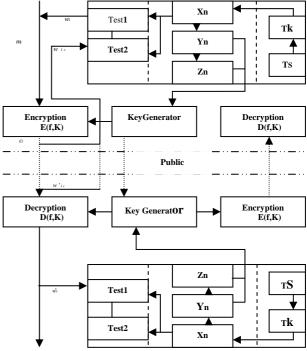


Figure 3. SSC Adaptive Stream Cipher Frame

The self-detection module consists of two detectors, which are responsible for adding state value W to the plaintext and testing encrypted state value  $W_{lc}$  and channel feedback state value  $w_{lc}$ . When the encrypted value and the channel feedback state values are abnormal, feedback should be done to the reference model, then the self-reference model will update with a new reference model according to the current state value of initial

algorithm, thus to update all the output signals of chaotic module, key generators will also update new key stream without changing of key k.

The receiver uses the decoder to return  $w_{k}$  to the sender first to confirm the acceptance of the cipher, at the same time offers  $W'_{1c}$ evaluation to the sender for the channel safety index testing thus to determine whether there is need to resend the cipher or renew the reference model. Receiver, with the key k got from the sender through the public channel transmission and the synchronize chaotic sequence key stream K generated by its synchronization reference model, and then get the plaintext  $\dot{m}$  and test value  $\dot{w}$  through decoder D, if  $\dot{w}$ matches with its own test value  $^{W}$  of the first stage  $X_n$ sequence of synchronization chaotic sequence with one's own first-level sequence, then it is confirmed that the received ciphertext is correct and decrypted successfully, if else, resend or renew reference model should be requested. The realization code and data format of selfadaptive SSC algorithm is shown in Figure 4 (each module operates in a separate thread to make sure the interrelated disruption of software is minimal)

```
public class Sende_Code
{ public static void main(String args[])
  Codemaker codemaker=new Codemaker();
  codemaker selfM odelThread start():
  codemaker.codeInOutThread.start();}
  class Codemaker implements Runnable
  { int countzero, countone, ki, mi.;
   Codemaker()
    { selfM odelT hread = new T hread(this);
    codeInOutThread=new Thread(this); }
   public void run()
    { for(int countone=1,true; countone++)
     setD ecision(countone);
    if (m e s s a g e = = S U C C E S S)
    return; }
public synchronized void setDecision(int countone)
{ if (Thread.current Thread()=selfM odelThread)
  {
             while(Testone& & Testtwo)
         \{ C \log k_M K(t); \}
           Logistic_MK(t); }
  }
  if (Thread.current Thread()=codeInOutThread)
  {
            while(Testone&&Testtwo)
         \{ C \log k_M R(t); \}
          Logistic_MR(t);}
          .....
  }
if (Thread.current Thread()=codeInOutThread)
{ for(int countzero = 0; ki& & mi; countzero + +)
  {keyFlow_maker();
   send_Message;}
}
}
```

```
%---
                     --Process_object--
%T(1)=0·
ŵT (2)=5*b_3*у4(1)/b_2;
%sum_out=0;
%if k>1
         for i=1 k
%
%
                 sum_out=sum_out+y4(i);
          end
%
         T (k+1) = (1*b_3*sum_out+0.5*b_1*T (k)-T (k-1))/b_2;
%
%end
                           -study_Process_4&3-----
<u>%</u>-
d4(1)=0:
d4(k)=(1.8-y4(k))
e (k+1)=d-y4 (k);
ee (k+1)=e (k+1)-e (k);
for i=1:5
        for i=1:5
               \begin{array}{l} \overset{}{d}_{0} \circ (i, j) = d4 \, (k) * y3 \, (i, j); \\ d3 \, (i, j) = d4 \, (k) * \circ (i, j); \end{array}
       end
end
%-
                        ---study_process_2-
d2=zeros(size(y2));
for j=1:5
        for i=1:5
                \frac{d2(1, j) = d2(1, j) + d3(j, i) * y2(2, i) * y2(1, j);}{d2(2, j) = d2(2, j) + d3(i, j) * y2(1, i) * y2(2, j); } 
        end
end
for
       i=1:2
        for j=1:5
               d_m(i, j)=d2(i, j)*2*(y1(i)-m(i, j))/q(i, j)^2; d_q(i, j)=d2(i, j)*2*(y1(i)-m(i, j))^2/q(i, j)^3
        end
end
                        ---modify parameters--
for i=1:2
        for j=1:5
                f_m(i,j)=sign(d_m(i,j)*d_m_1(i,j));
                f_q(i, j) = sign(d_q(i, j) * d_q_1(i, j));
               \mathbf{n}_{\mathbf{m}}(\mathbf{i},\mathbf{j})=2\hat{\mathbf{f}}_{\mathbf{m}}(\mathbf{i},\mathbf{j})*\mathbf{n}_{\mathbf{m}}(\mathbf{i},\mathbf{j});
               \begin{array}{l} \underline{n}_{q}(i,j) = 2^{f} \underline{f}_{q}(i,j) * \underline{n}_{q}(i,j); \\ \underline{n}(i,j) = m(i,j) + \underline{n}_{m}(i,j) * \underline{d}_{m}(i,j); \end{array}
               q(i,j)=q(i,j)+n_q(i,j)*d_q(i,j);
d_m_1(i,j)=d_m(i,j);
                d_q_1(i,j)=d_q(i,j);
        end
end
for i=1:5
        for j=1:5
               \begin{array}{l} \overbrace{f_{o}(i,j)=sign(d_{o}(i,j)*d_{o}-1(i,j));} \\ n_{o}(i,j)=1,1^{\circ}f_{o}(i,j)*n_{o}(i,j); \\ o(i,j)=o(i,j)+n_{o}(i,j)*d_{o}(i,j); \end{array}
                d_0_1(i,j) = d_0(i,j);
        end
end
k=k+1:
end . .
```

Figure 4. Self-adaptive SSC Code and Data Format

## *B. Key Generator Pseudo-random Horizontal Level Disturbance Design*

There exist a fundamental relation between the order of differentiation of an operator, its code space and the required accuracy. For smaller s the Fourier transform of the kernel increases in width, at a certain space giving rise to aliasing. In theory this occurs at all space due to the infinite extent of the exponential function, but it becomes apparent at smaller space. The 'leaked' information is folded back, in theory even from all further periods as well, so the amplitude no longer represents the accurate value of 'pure' differentiation. We consider the power spectrum, i.e. the square of the signal. The aliasing differential operator integral operator and the error can be defined as the relative integrated energy of the aliased frequencies over the total energy that could do the operation on the data as it for the mapping image of the database, so the following operation is done corresponding to the feature extracting and encryption to select the key-point information of the Stream Cipher [22]:

$$\begin{split} c(\vec{x},t) &= c\left(\|\nabla L(\vec{x},t)\|\right) = e^{-\left(\frac{\|\nabla L\|^2}{k^2}\right)} \\ &\frac{\partial L}{\partial s} = \nabla \cdot (c \,\nabla L) \\ E_{xy} &= \sum_{u,v} W_{u,v} [xI_x + yI_y + O(x^2, y^2)]^2 \approx (x,y) M(x,y)^T \\ error(n,\sigma) &= \frac{\Delta E_n(\sigma,\omega)}{E_n(\sigma,\omega)} \\ &= \frac{\int_{\pi}^{\infty} \omega^{2n} e^{-\sigma^2 \omega^2} d\omega}{\int_0^{\infty} \omega^{2n} e^{-\sigma^2 \omega^2} d\omega} \\ \sigma_{i,j} &= \frac{\frac{1}{n} \sum_{l=-k}^k \sum_{m=-k}^k (p_{l,m} - \alpha) (q_{i+l,j+m} - \beta)}{\sqrt{\sigma(p) \,\sigma(q)_{i,j}}}. \end{split}$$

$$\begin{split} C_1(x,y) &= \widehat{I_x^2} \widehat{I_y^2} - (\widehat{I_x} \widehat{I_y})^2 \\ &= \sum_{x_1,y_1} g(x_1,y_1) I_x^2 (x-x_1,y-y_1) \sum_{x_2,y_2} g(x_2,y_2) I_y^2 (x-x_2,y-y_2) \\ &- \left[ \sum_{x_1,y_1} g(x_3,y_3) I_x (x-x_3,y-y_3) I_y (x-x_3,y-y_3) \right]^2 \\ &= \sum_{x_1,y_1} \sum_{x_2,y_2} g(x_1,y_1) g(x_2,y_2) \left[ I_x^2 (x-x_1,y-y_1) I_y^2 (x-x_2,y-y_2) \\ &- I_x (x-x_1,y-y_1) I_y (x-x_1,y-y_1) I_x (x-x_2,y-y_2) I_y (x-x_2,y-y_2) \right] \\ &= \frac{1}{2} \sum_{x_1,x_2,y_1,y_2} g(x_1,y_1) g(x_2,y_2) \left[ I_x (x-x_1,y-y_1) I_y (x-x_2,y-y_2) \\ &- I_x (x-x_2,y-y_2) I_y (x-x_1,y-y_1) I_y (x-x_2,y-y_2) \right] \\ &= \frac{1}{2} \sum_{x_1,x_2,y_1,y_2} g(x_1,y_1) g(x_2,y_2) \left[ (I_x (x-x_1,y-y_1), I_y (x-x_1,y-y_1)) (-I_y (x-x_2,y-y_2) \\ &- I_x (x-x_2,y-y_2) \right]^2. \end{split}$$

The input terminus of key generator consists of the initial key K and the output signal of the second stage Logistic, the initial key and the third-level Logistic signal generate round keys, the round keys and the second stage chaotic signal generated key stream K through calculation . The structure is shown in Figure 5.

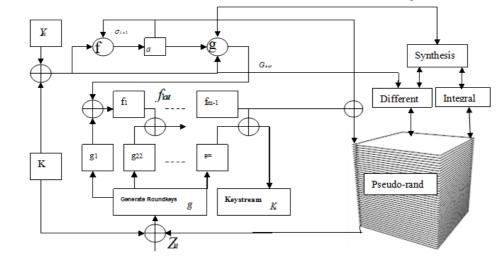


Figure 5. Pseudo-random horizontal level disturbance key generator

The output relationship of each module is as follows

$$\sigma_{i} = f[\sigma_{i+1}, (Y_{n} \oplus K)]$$

$$G_{out} = g[\sigma_{i}, (Y_{n} \oplus K)]$$

$$f_{1out} = f_{1}(g_{out} \oplus g_{1})$$

$$f_{iout} = f_{i}(f_{i-1out} \oplus g_{i}), \text{ m-}2 \ge i \ge 2$$

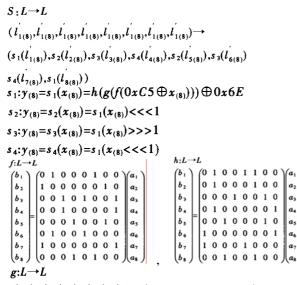
$$K_{i} = f_{m-1} \oplus g_{m}$$

$$g_{i} = \text{mod}_{i}(K \oplus Z_{n})$$

$$\sum_{i \neq i} f_{m} = f_{m} =$$

The calculation process is as follows [23] ~ [25]: (1) with initial data packet key and Yn bitwise XOR added transform the initial round keys. (2) Cycle m-2 round of data processing, 4 steps in a round

1) s box transform [26] ~ [28]: S box applying Camellia algorithm, it's a reversible transform on  $GF(2^*)$ , which can increase the safety and contribute to the hardware miniaturization of the algorithm. As it has been proved that the minimum value the maximum differential probability of the function on  $GF(2^*)$  is  $2^{-6}$ , and it is speculated that the minimum value of the maximum linear probability is also  $2^{-6}$ . So choose the reversible function which can get the best differential and linear probability at  $GF(2^*)$  as S box, meanwhile, the output bite of S box has higher-order Boolean polynomial which makes the high-order differential attack for the algorithm quite difficult. In addition, the complex expressions of S box at  $GF(2^*)$  output and input functions makes the insert attack to the algorithm less effective. The bytes replacement of s box:



 $(b_{1},b_{2},b_{3},b_{4},b_{5},b_{6},b_{7},b_{8}) = g(a_{1},a_{2},a_{3},a_{4},a_{5},a_{6},a_{7},a_{8})$  $(b_{8}+b_{7}\alpha+b_{6}\alpha^{2}+b_{5}\alpha^{3}) + (b_{4}+b_{3}\alpha+b_{2}\alpha^{2}+b_{1}\alpha^{3})\beta$ 

 $= 1 / ((a_8 + a_7 \alpha + a_6 \alpha^2 + a_5 \alpha^3) + (a_4 + a_3 \alpha + a_2 \alpha^2 + a_1 \alpha^3))$ 

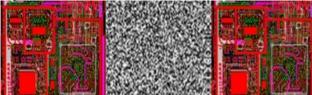
2) Line shift operation: take row cycle left shift operation;3) Column confusion: take matrix multiply each column grouping;

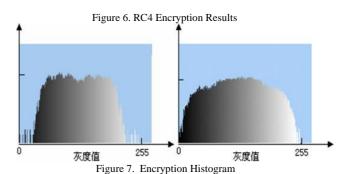
4) Round key and operation: the initial key and Zn data block bitwise XOR, round keys are generated after expansion, with the current round keys and the current round of data packets by bit XOR

(3) The final round of transformation. It has 3 steps: 1) s box transforms; 2) the line shift operation; 3) round key addition operation. Algorithm is as mentioned above.

### IV. PRACTICAL APPLICATION OF THE MODEL

RC4 algorithm source code was publised in 1994 in Cypherpunks's mailing list. RC4 can be simply described as: non-linear part is an8-in-and-8-out S-box, is the displacement from 0 to 255, and the replacement is a key function of a variable length. We directly use RC4 to encrypte a binary image, see Figure 6





Key generation algorithm and encryption algorithm are:

for (i=0, i<255,i++) {S[i] = i;j =0;} for (i=0, i<255,i++) {j = (j + S[i] + k[i]) (mod 256); swap (S[i], S[j]);} int i = j = 0; //for each message byte Mi// i = (i + 1) (mod 256); j = (j + S[i]) (mod 256); swap(S[i], S[j]); t = (S[i] + S[j]) (mod 256); Ci = Mi XOR S[t]; Example a subservation in a subservation

Encryption results is shown in Figure 6 (middle); encryption effect histogram is in Figure 7 (left); when the decryption side access key K, through the decryption algorithm D, the image accessed is as in Figure 6(right).



Figure 8. Decryption Results

Revise RC4 algorithm according to self-adaptive SSC protocol model, the codes referred to Section 2 and 3 of this paper, the current PC clock is taken as the initial clock of reference model parameters in self-adaptive control part, encrypt the Figure 6 (left), the result is asFigure 8 (left); when decryption side has the synchronized self-adaptive module and obtains the key K, take out the decryption and get Figure 8 (middle); if the decryption side synchronized self-adaptation module does not work but still has a key K, the decrypted image is as Figure 8 (right) shows.

	Cycle count	Time(µS)	Pg(data)	Data/S	Stable-AA(ip)	Stable-BB(ip)
Key Generator	13659	1358.34	152	C9, T28	0.6367	0. 41063
Encryption Progress	3012	274.31	70	163052	0.8253	0. 69243
Decryption Progress	3032	280.22	73	163179	0.8359	0. 77635
Total Progress	19703	1912.87	295	326231	0. 7737	0. 57282

Table 1 RC4 Operating Parameters

It can be seen that the improved RC4 algorithm based on SSC self-adaptive protocol model can not decrypt the original image with key k, decryption function E and key generator neither the bytes files. So we can say the

key K can be transmitted in the public channel, a better randomness can be expected from the encryption histogram of the new model. The operating parameters of the process is shown in Table 1.

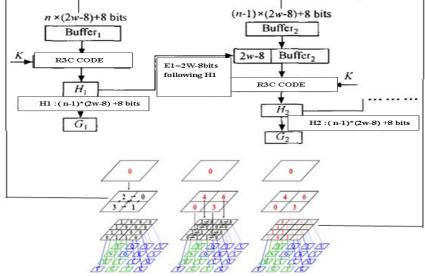


Figure 9. Improved RC5 Method

We also have done the improved RC5 algorithm that can be used in the real-time work, then we take an experiment on the new frame of the RC5 (Fig 9). When encrypt the index value of the leaf nodes, the ciphertext of previous node are taken as part of the plaintext of the next level, use RC5 keys to encrypt it together with the data of next leaf nodes. But the number of nodes and the encrypted results of the same level are put together into the encryption algorithm, that's to say, the encryption process of nodes number are taken as nested subalgorithm to create multiple encrypted results. See Figure 5. Conduct a circulate encryption algorithm to m Buffer of this plaintext, use R3C encryption algorithm in each region. If the length of plaintext is between m-Buffer and m +1 Buffer, then RC5 encryption are done on the part beyond the m-Buffer separately. While here m may have different values, which makes it easier to control the size of the data. The operating results can be seen from the Fig10 and the parameters of the process is shown in Table 2.

	Cycle count	Time(µS)	Pg(data)	Data/S	Stable-AA(ip)	Stable-BB(ip)
Key Generator-A	8659	658.34	722	C9, T28	0.7467	0. 42636
Encryption Progress-A	1012	134. 31	34	193061	0. 9483	0.76243
Decryption Progress-A	1072	156. 22	34	18159	0. 9389	0.83635
Total Progress-A	10743	948.87	790	136537	0. 8937	0.67282
Key Generator-B	7328	623. 78	843	C5, T49	0. 7345	0. 4978
Encryption Progress-B	1235	198. 45	28	146575	0.9934	0.93684
Decryption Progress-B	1127	183. 56	29	17239	0. 9948	0.98467
Total Progress-B	11038	964.03	894	158344	0. 9473	0.89426
Total Progress-	10782	956. 31	852	147294	0. 9059	0. 77375

Table 2 RC5 Operating Parameters

```
void main()
{ WORD i, j, pt1[2], pt2[2], ct[2] = {0,0};
  unsigned char key[b];
  if (sizeof(WORD)!=4)
    printf("RC5 error: WORD has %d bytes.\n",sizeof(WORD));
  printf("RC5-32/12/16 examples:\n");
  for (i=1:i<6:i++)
    { /* Initialize pt1 and key pseudorandomly based on previous ct */
     pt1[0]=ct[0]; pt1[1]=ct[1];
      for (j=0;j<b;j++) key[j] = ct[0]%(255-j);</pre>
      /* Setup, encrypt, and decrypt */
     RC5_SETUP(key);
     RC5_ENCRYPT(pt1,ct);
     RC5_DECRYPT(ct,pt2);
      /* Print out results, checking for decryption failure */
     printf("\n%d. key = ",i);
      for (j=0; j<b; j++) printf("%.2%",key[j]);</pre>
             pt1[0], pt1[1], ct[0], ct[1]);
      if (pt1[0]!=pt2[0] || pt1[1]!=pt2[1])
        printf("Decryption Error!");
    3
}
```

It can be seen that the improved RC5 algorithm based on SSC self-adaptive protocol model also can not decrypt the original image with key k, decryption function E and key generator neither the bytes files. The key K can be transmitted from the public channel.

#### V. CONCLUSION

The reference model of self-adaptive controller takes the Minimum power clock function control module by a

parallel clock-control sequence and chaotic cascade function made of multiple Logistic function, which is assigned a pair of encryption and decryption, through the same initialization of as the stimulus unit, the unit is theoretically synchronous, but no stability tests carried out on the actual system for a long period, and there should be a certain redundancy in the actual transmission between the sender and the receiver; key generator consists of the most safe and efficient parts of sophisticated block algorithm, AES and Camellia, but compares with the traditional stream cipher model, these parts have no advantage in speed. ith the public algorithm analysis being more sophisticated, the safety of the key generator will face new challenges. In this paper, the test printf("\n plaintext %.81% %.81% ---> ciphertext %.81% %.81% \n" of the modified version of RC4 and RC5 algorithm has been carried out, but some other algorithms, such as A5, E0, etc. also need to be modified to test the applicability

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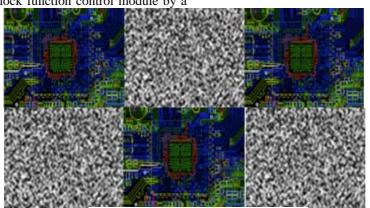


Figure 10. RC5 Encryption and Decryption Results

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